



FACULTY PROFILE

Name	Mrs. M. Dharani					
Gender	Female	Date Of Birth	10.06.1981	Age	42 Years	
Present Designation	Assistant Professor		Regular / Adjunction	Regular		
Date of Joining	01.03.2024					
Mobile No.	9791745088		Email	Dharanim.eee@cahcet.edu.in		
Highest Educational Qualification	M.Tech.		Specialization	Applied Electronics		
Additional Qualification	GATE/NET/SLET	Nil				
Total Working Experience	19 years	(i) Teaching			: 19 Years	
		(ii) Industry			: Nil	
Publications	Journals	08	International Journals	08	National Journals	Nil
	Conferences	07	International Conferences	03	National Conferences	11
No. of Patents/Project Proposals	Project Proposals: Nil					
No. of Workshops/Seminars/Conferences/FDPs attended	Workshops: 19; Seminars/Webinars: 45; FDPs: 21; Short Term Training Programme: 05					
No. of Workshops/Seminars/Conferences/FDP organized	Workshops: 02; Seminars: 01; FDPs: Nil					
No of UG / PG / Ph.D. Guidance:	UG:45 PG:Nil Ph.D: Nil					
Membership in Professional Bodies	Nil					
Awards / Recognition Received	Received Certificate of Honor for 100% result					
Other Responsibilities, if any	Nil					

JOURNAL DETAILS

Publications

RESEARCH PUBLICATIONS (INTERNATIONAL JOURNALS) (8):

- MahalingamDharani, and Ravi Kuppan**“Challenges and Advancement in Electric Vehicle Wireless Charging Scheme with Minimising Environmental Issues” Journal of Environmental Protection and Ecology, Vol. 23, Issue 8 (2022): pp. 3449–3459. (**Science Citation Index, Impact factor: 0.538**).
- Dharani. M, and Dr. Ravi K,** "Systematic literature review on wireless power transmission." Turkish Journal of Computer Mathematics Education 12 (2021), pp. 4400-4406. (**Scopus Indexed**).



3. **Dharani M, Lakshmikhandan K, Dr.AndySrinivasan**, “Performance Analysis of 11-Multilevel Inverters using Sinusoidal PWM Techniques with Reduced Switching Pattern”, Asian Journal of Research in Social Science and Humanities, Vol. 7, No. 3, pp. 791-801, ISSN 2249-7315, (March 2017). (**Annexure 1**)
4. **Dharani. M,Lakshmikhandan K, Srinivasan A**, “Design of Single Phase Nine Level Inverter for an Unbalanced Capacitor”, Middle-East Journal of Scientific Research, pp. 74- 80, ISSN 1990-9233, (2016). (**Annexure 2**)
5. **Dharani M, Lakshmikhandan K, Andy Srinivasan**, “Improved SPWM Modulation Technique to Reduce Harmonic Distortion in Voltage Source Converter”, International Journal of Innovation and Scientific Research”, ISSN 2351-8014 Vol. 25 No. 1 (Jun. 2016), pp. 8-14. (**Annexure 2**)
6. **Dharani. M,Lakshmikhandan K, Srinivasan A**,“Optimal switching angle selection and harmonic elimination of multilevel inverter: A new hybrid technique”, International Journal of Applied Engineering Research, ISSN 0973-4562 Volume 9, Number 23 (2014)pp. 21921-21940. (**Annexure 2**)
7. **Lakshmikhandan K, Dharani M &Srinivasan A**, “Modeling and Analysis of Hybrid Filters for Harmonic Reduction in Medium Voltage Distribution Network”, International Journal of Applied Engineering Research, ISSN 0973-4562 Volume 9, Number 24 (2014)pp. 29193-29200. (**Annexure 2**)
8. **Dharani M, Prathima V, Shruthi E & Salma N**, “Bee Colony Optimization for Selective harmonic elimination PWM in Multilevel Inverter”, International Journal of Engineering Research and Sports Science, ISSN 2348-1404 (2014).(**Scopus Indexed**)

INTERNATIONAL & NATIONAL CONFERENCES (14):

1. **M. Dharani (2023)**,has participated and presented a paper on “**An analytical study for the realization of maximum efficiency in wireless charging system using a circular inductive coil**”in the Three days 4th International conference on Innovations in Power and Advanced Computing Technologies (i-PACT - 2023)held during 8thto 10thDecember 2023, at Malaysia.
2. **M. Dharani (2019)**, has participated and presented a paper on “**Design and Implementation of closed loop Bi-directional converter fed with PMDC drive**”,in the Two days National Conference on “Recent Innovations in Engineering Technology, Management and Applications (RIETMA-2019)” held during 15th& 16th March 2019 at Priyadarshini Engineering College, Vaniyambadi.
3. **M. Dharani (2019)**, has participated and presented a paper on “**Reduction of THD and Boosting the output voltage using CSC-MLI**”,in One day National Conference on “Recent Trends in Electrical, Electronics and Communication Engineering (RTEECE’19)” held on 12th March 2019at Sri BalajiChockalingam Engineering College, Arni.
4. **M. Dharani (2018)**, has participated and presented a paper on“**A Novel design of Square T-Type module for Multilevel Inverter fed Induction Motor drive using NLC scheme**”in the Two days National Conference on “Recent Innovations in Engineering Technology, Management and Applications (RIETMA-2018)” held during 6th& 7th April at Priyadarshini Engineering College, Vaniyambadi.
5. **M. Dharani (2017)**,has participated and presented a paper on“**A New topology of single phase 49 level Inverter using Switched capacitor DC to DC converter**”, in IEEE International Conference on Engineering and Applied Sciences (ICEAS’17), 22nd to 24th March 2017 at S.R.I College of Engineering and Technology, Vandavasi.
6. **M. Dharani (2016)**,has participated and presented a paper on“**Enhancement of power quality for Electric vehicle using transformerless Hybrid series active filter**”, in International Conference on Recent Advances in Technology, Engineering and Science (ICRATES’16), 22th to 28th July 2016 at C. Abdul Hakeem College of Engineering and Technology, Melvisharam.



7. **M. Dharani (2016)**, has participated and presented a paper on “**Design of Single phase Nine level Inverter for an Unbalanced capacitor**” in the Two days National Conference on “Recent Innovations in Engineering Technology, Management and Applications (RIETMA-2016)” held during 15th&16th April 2016, at Priyadarshini Engineering College, Vaniyambadi.
8. **M. Dharani (2016)**, has participated and presented a paper on “**Enhancement of Power Quality using Transformerless Hybrid Series Active filter for Electric Vehicle**”, in the Two days National Conference on “Applications of Biomedical Engineering Systems and Technologies – Current Trends and Future Scenario “A” Best 2016” held during 23rd& 24th March 2016, at KarpagaVinayaga College of Engineering and Technology, Kancheepuram.
9. **M. Dharani (2015)**, has participated and presented a paper on “**A Solar Power generation system with a Seven level Inverter**”, in the Two days National Conference on “Innovations in Science, Engineering and Technology, ISET-2015” held during 27th& 28th March 2015 at Podhigai College of Engineering and Technology, Thirupattur.
10. **M. Dharani (2015)**, has participated and presented a paper on “**A Novel Technique for solar power generating system using multilevel inverter**”, in One day National Conference on “Recent Trends in Electrical, Electronics and Communication Engineering, RTEECE-15” held on 23rd March 2015 at Sri BalajiChockalingam Engineering College, Arni.
11. **M. Dharani (2015)**, has participated and presented a paper on “**Equivalent Circuit based core loss Modelling for Electric Power Steering Application**”, in One day National Conference on “Recent Trends in Power Electronics and Power Systems, RTPEPS-15” held on 13th March 2015 at SKP Engineering College, Thiruvannamalai.
12. **M. Dharani (2014)**, has participated and presented a paper on “**Bee Colony Optimization for Selective Harmonic Elimination PWM in Multilevel Inverter**”, in One day National Conference on “Recent Advances in Communication Engineering, NCRACE-14” held on 6th March 2014 at Sri BalajiChockalingam Engineering College, Arni.
13. **M. Dharani (2012)**, has participated and presented a paper “**A Hybrid Active filter for a 3 phase 12-pulse diode rectifier used for a medium voltage Motor drive**”, in One day National Conference on “Advancements in Electrical Science & Technology 2K12” held on 22nd August 2012 at Magna College of Engineering, Chennai.
14. **M. Dharani (2010)**, has participated and presented a paper “**Design and Implementation of bi-directional converter with I-V system using FLC**”, in One day National Conference on “Mobile and Ubiquitous Computing” held on 19th March 2010 at Magna College of Engineering, Chennai.